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April 20, 2004

WRITER'S DIRECT NUMBER: (202) 772-8629 **INTERNET ADDRESS:** DONF@SKGF.COM

Art Unit 2811

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Re:

U.S. Utility Patent Application

Application No. 10/697,079; Filed: October 31, 2003

Memory Cell for Modification of Revision Identifier in an Integrated

Circuit Chip

Inventors:

CATALASAN et al.

Our Ref:

1875.4360002

Sir:

Transmitted herewith for appropriate action are the following documents:

- 1. Preliminary Amendment Under 37 C.F.R. § 1.115;
- 2. Information Disclosure Statement;
- Form PTO-1449 listing the cited documents (1 page); 3.
- 4. Copies of the three (3) cited documents; and
- 5. Return postcard

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Donald J. Featherstone Attorney for Applicants Registration No. 33,876

DJF/lvt 254311_1.DOC